PMEG2020CER-Q

20 V, 2 A Schottky barrier rectifier

24 August 2023 Product data sheet

1. General description

Planar Schottky barrier rectifier encapsulated in a CFP3 (SOD123W) power flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- High power capability due to clip bond package
- Small and flat lead SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

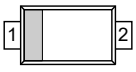
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	δ = 0.5; f = 20 kHz; square wave; T_{sp} ≤ 167 °C		-	-	2	A
V_R	reverse voltage	T_j = 25 °C		-	-	20	V
V_F	forward voltage	I_F = 2 A; pulsed; T_j = 25 °C	[1]	-	460	520	mV
I_R	reverse current	V_R = 20 V; pulsed; T_j = 25 °C	[1]	-	8	50	µA
		V_R = 20 V; pulsed; T_j = 125 °C	[1]	-	5	25	mA

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 CFP3 (SOD123W)	 sym001
2	A	anode		

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2020CER-Q	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2020CER-Q	N3

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	T_j = 25 °C		-	20	V
I_F	forward current	δ = 1; T_{sp} ≤ 165 °C		-	2.8	A
$I_{F(AV)}$	average forward current	δ = 0.5; f = 20 kHz; square wave; T_{sp} ≤ 167 °C		-	2	A
I_{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	50	A
P_{tot}	total power dissipation	T_{amb} ≤ 25 °C	[1]	-	0.68	W
			[2]	-	1.15	W
T_j	junction temperature			-	175	°C
T_{amb}	ambient temperature			-55	175	°C
T_{stg}	storage temperature			-65	175	°C

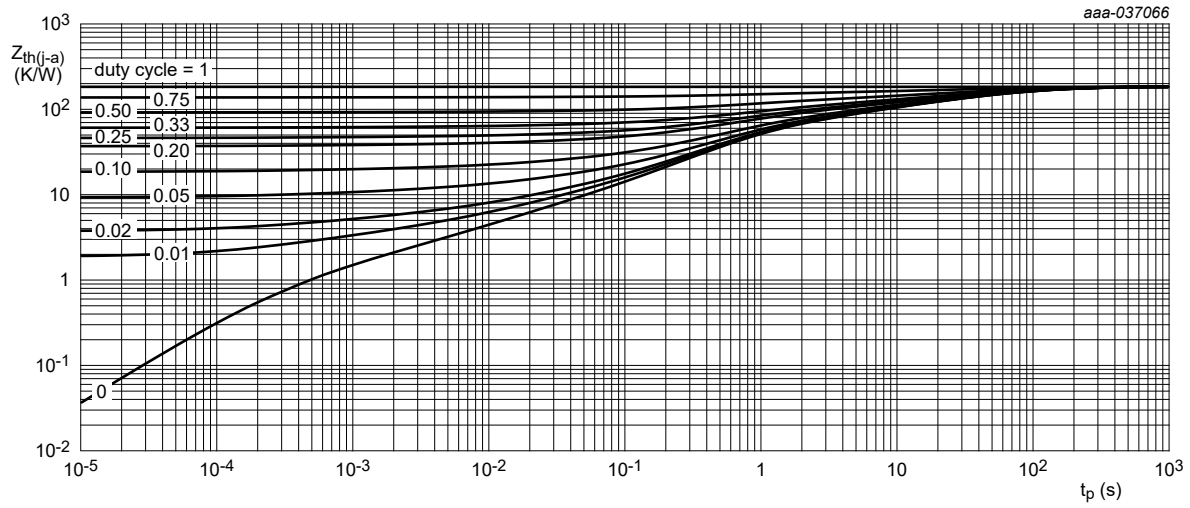
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm^2.

9. Thermal characteristics

Table 6. Thermal characteristics

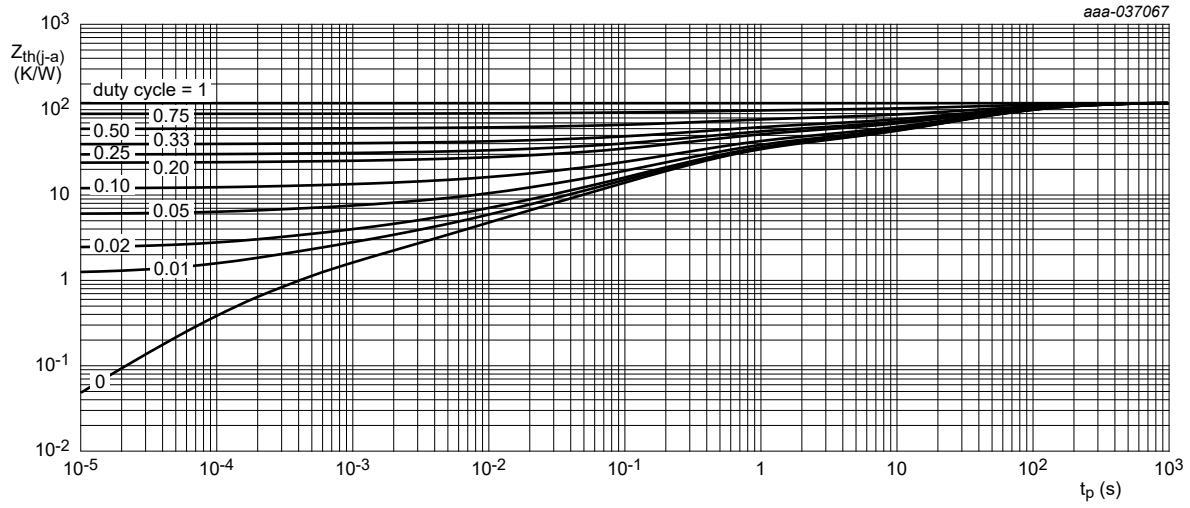
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	220	K/W
			[3] [2]	-	-	130	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	18	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm^2.
[4] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

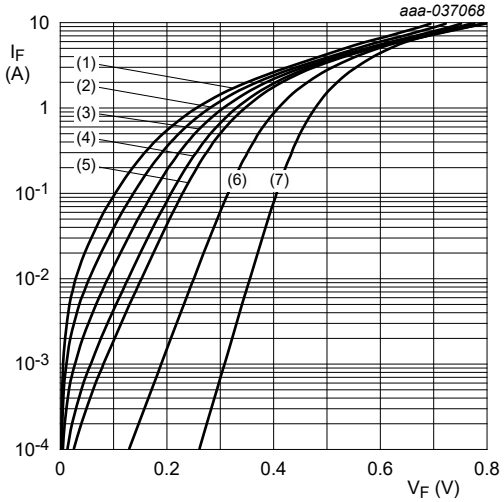
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

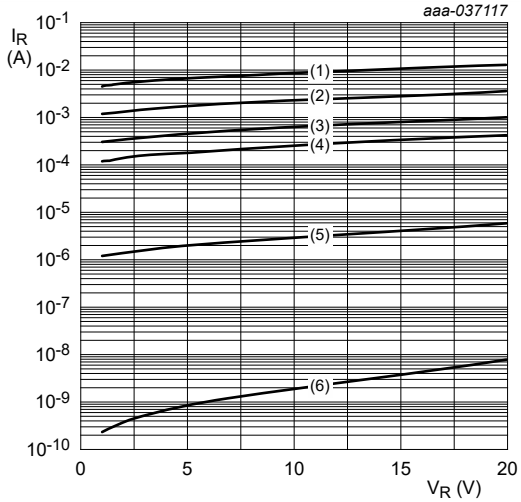
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	I_R = 1 mA; pulsed; T_j = 25 °C	[1]	20	-	-	V
V_F	forward voltage	I_F = 1 A; pulsed; T_j = 25 °C	[1]	-	405	450	mV
		I_F = 2 A; pulsed; T_j = 25 °C	[1]	-	460	520	mV
		I_F = 2 A; pulsed; T_j = -40 °C	[1]	-	520	590	mV
		I_F = 2 A; pulsed; T_j = 125 °C	[1]	-	380	470	mV
I_R	reverse current	V_R = 20 V; pulsed; T_j = 25 °C	[1]	-	8	50	µA
		V_R = 20 V; pulsed; T_j = 125 °C	[1]	-	5	25	mA
C_d	diode capacitance	V_R = 1 V; f = 1 MHz; T_j = 25 °C		-	145	-	pF
		V_R = 10 V; f = 1 MHz; T_j = 25 °C		-	51	-	pF
t_{rr}	reverse recovery time step recovery	I_F = 0.5 A; I_R = 0.5 A; $I_{R(meas)}$ = 0.1 A; T_j = 25 °C		-	5.5	-	ns
	reverse recovery time ramp recovery	dI_F/dt = 200 A/µs; I_F = 6 A; V_R = 26 V; T_j = 25 °C		-	5.5	-	ns
I_{RM}	peak reverse recovery current			-	0.6	-	A
Q_{rr}	reverse recovery charge			-	2.1	-	nC
V_{FRM}	peak forward recovery voltage	I_F = 0.5 A; dI_F/dt = 20 A/µs; T_j = 25 °C		-	390	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.



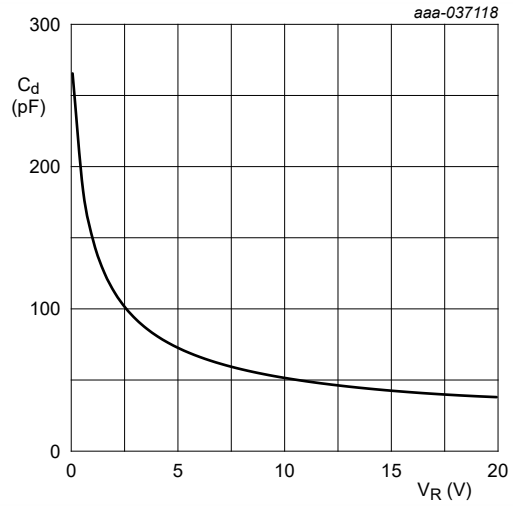
(1) T_j = 175 °C
(2) T_j = 150 °C
(3) T_j = 125 °C
(4) T_j = 100 °C
(5) T_j = 85 °C
(6) T_j = 25 °C
(7) T_j = -40 °C

Fig. 3. Forward current as a function of forward voltage; typical values



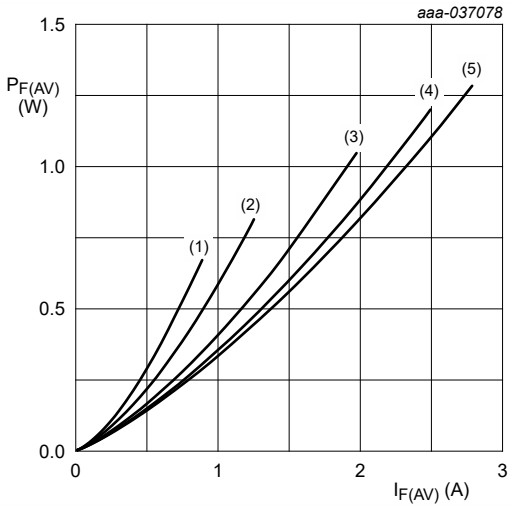
(1) T_j = 150 °C
(2) T_j = 125 °C
(3) T_j = 100 °C
(4) T_j = 85 °C
(5) T_j = 25 °C
(6) T_j = −40 °C

Fig. 4. Reverse current as a function of reverse voltage; typical values



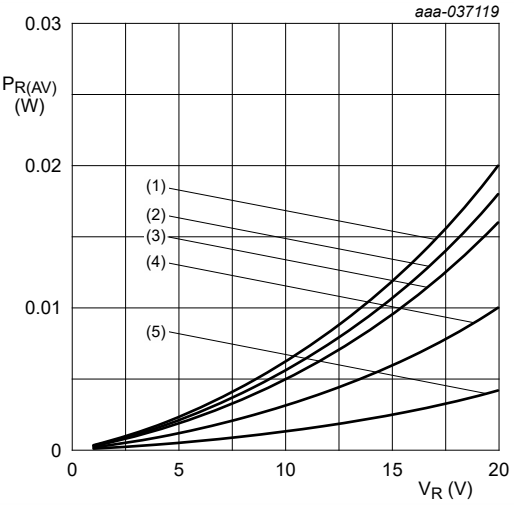
f = 1 MHz; T_{amb} = 25 °C

Fig. 5. Diode capacitance as a function of reverse voltage; typical values



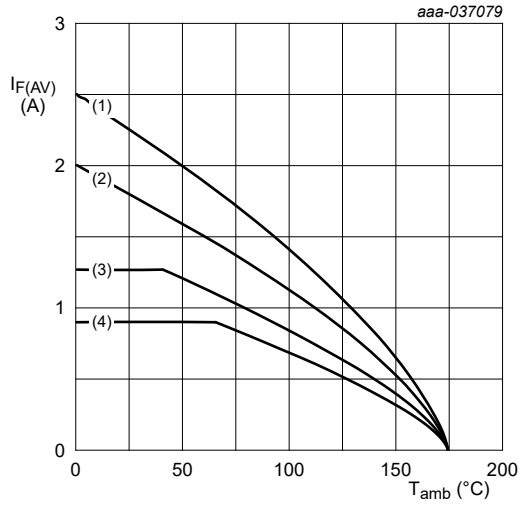
T_j = 100 °C
(1) δ = 0.1
(2) δ = 0.2
(3) δ = 0.5
(4) δ = 0.8
(5) δ = 1

Fig. 6. Average forward power dissipation as a function of average forward current; typical values



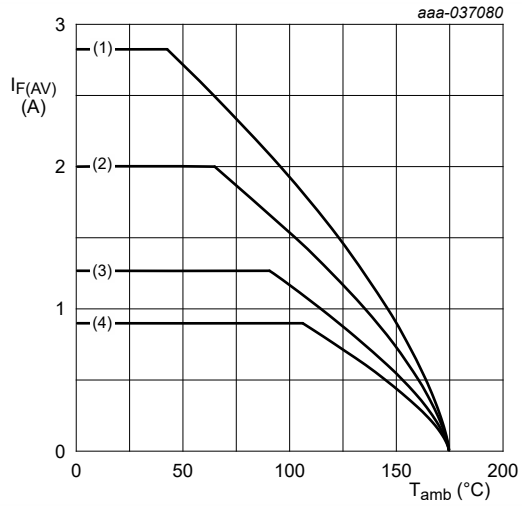
T_j = 100 °C
(1) δ = 1
(2) δ = 0.9
(3) δ = 0.8
(4) δ = 0.5
(5) δ = 0.2

Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



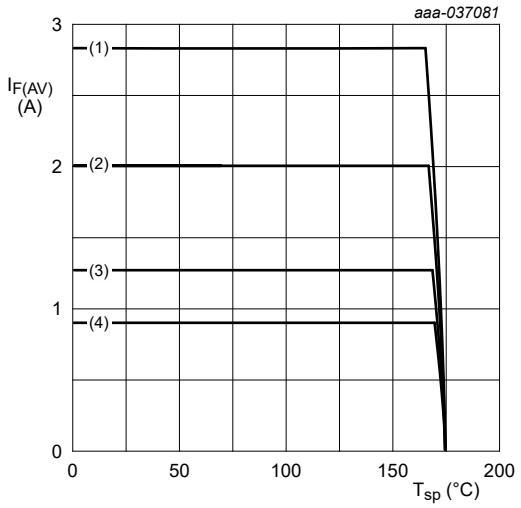
FR4 PCB, standard footprint
T_j = 175 °C
(1) δ = 1; DC
(2) δ = 0.5; f = 20 kHz
(3) δ = 0.2; f = 20 kHz
(4) δ = 0.1; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm^2
T_j = 175 °C
(1) δ = 1; DC
(2) δ = 0.5; f = 20 kHz
(3) δ = 0.2; f = 20 kHz
(4) δ = 0.1; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



T_j = 175 °C
(1) δ = 1; DC
(2) δ = 0.5; f = 20 kHz
(3) δ = 0.2; f = 20 kHz
(4) δ = 0.1; f = 20 kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values

11. Test information

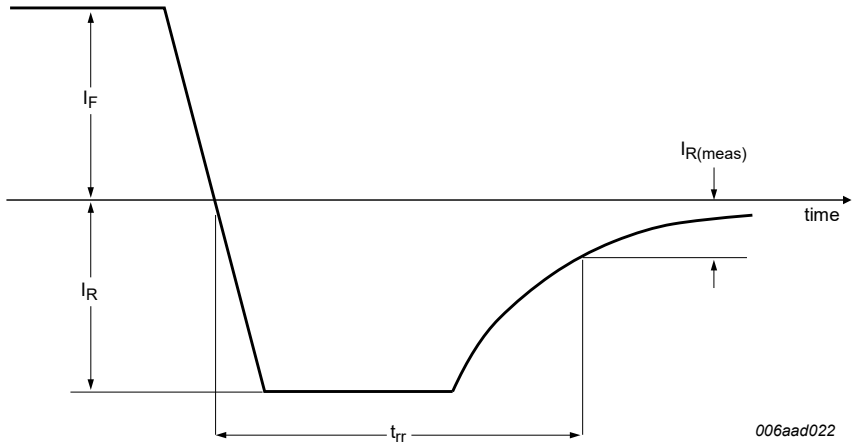


Fig. 11. Reverse recovery definition; step recovery

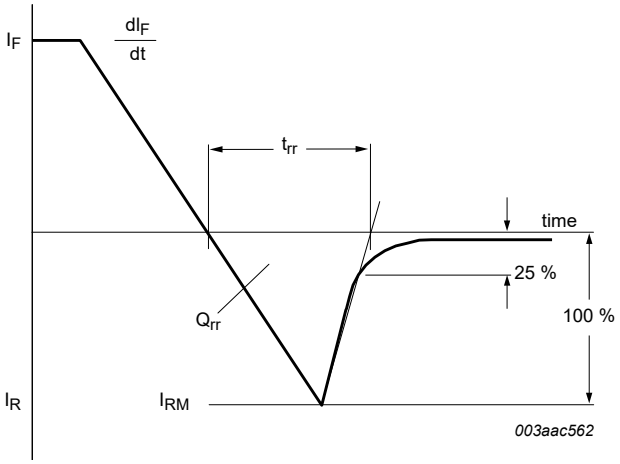


Fig. 12. Reverse recovery definition; ramp recovery

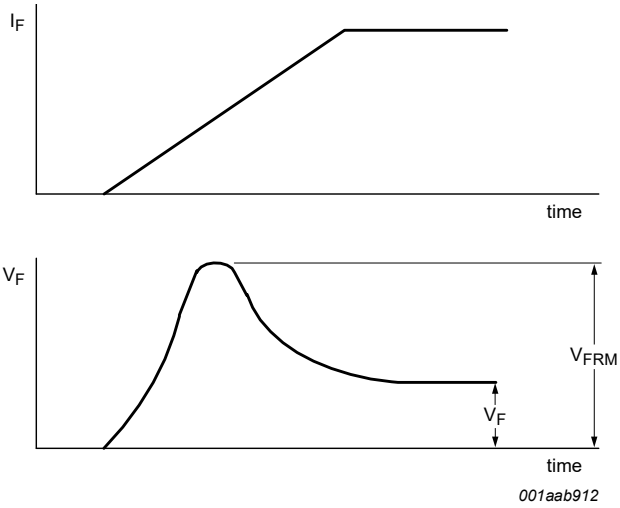


Fig. 13. Forward recovery definition

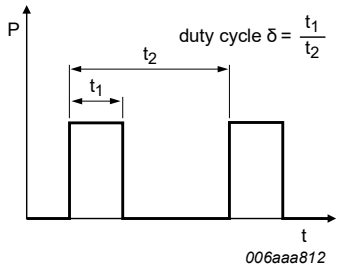


Fig. 14. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$I_{F(AV)}=I_M \times \delta$ with I_M defined as peak current

$I_{RMS}=I_{F(AV)}$ at DC, and $I_{RMS}=I_M \times \sqrt{\delta}$

with I_{RMS} defined as RMS current.

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

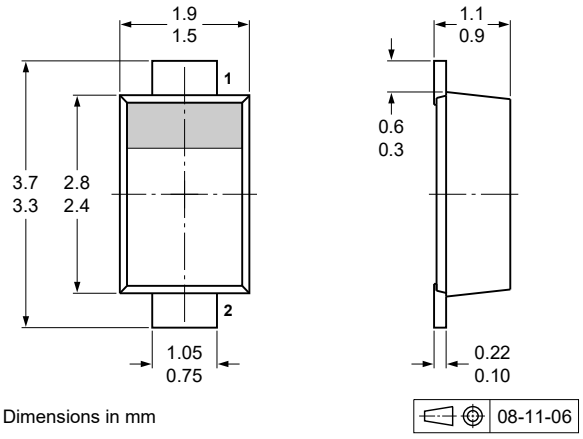
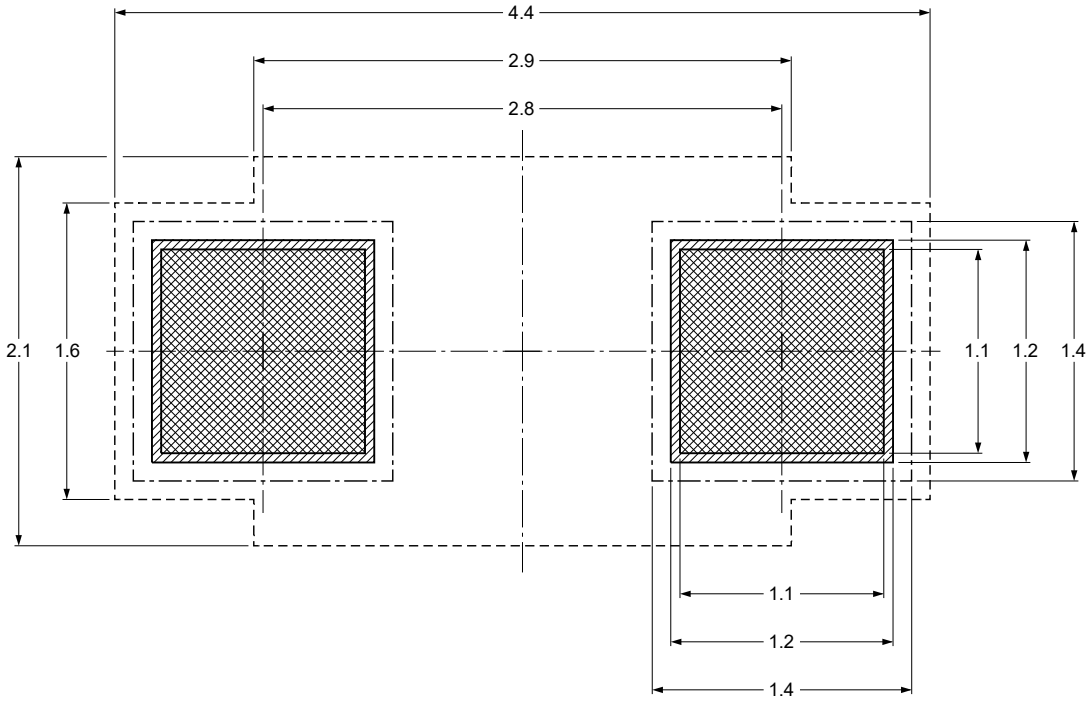


Fig. 15. Package outline CFP3 (SOD123W)

13. Soldering

Footprint information for reflow soldering of CFP3 package **SOD123W**



recommended stencil thickness: 0.1 mm

occupied area

solder resist

solder land

solder paste

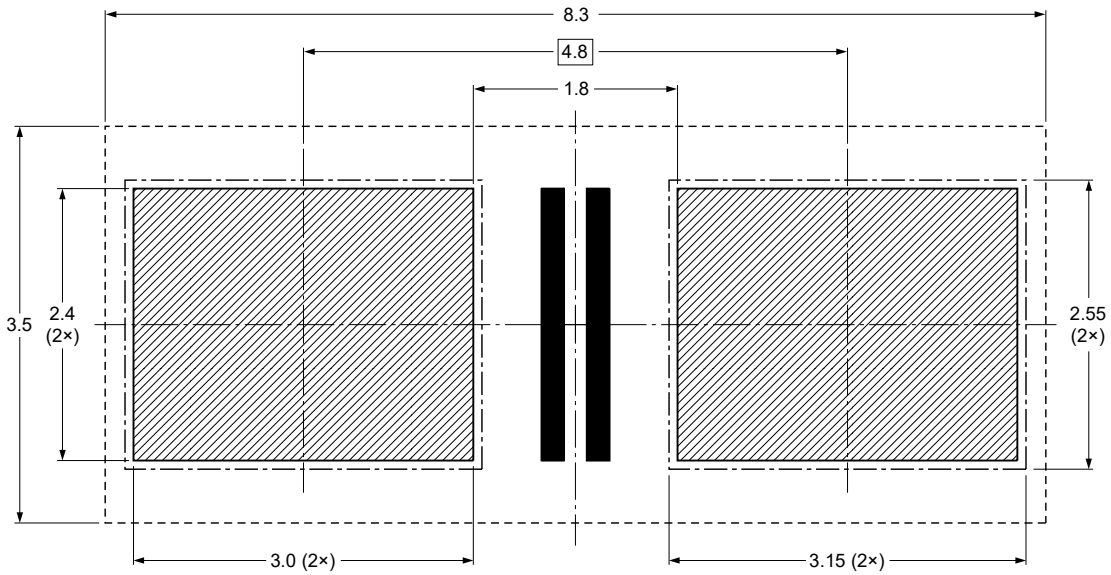
Dimensions in mm

Issue date ~~17-06-09~~ 20-02-28

sod123w_fr

Fig. 16. Reflow soldering footprint for CFP3 (SOD123W)

Wave soldering footprint information **SOD123W**



occupied area

solder resist

solder lands

dummy track (solder resist and Cu free)

Dimensions in mm

Issue date ~~17-06-06~~ 17-06-07

sod123w_fw

Fig. 17. Wave soldering footprint for CFP3 (SOD123W)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2020CER-Q v.1	20230824	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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